CONNECTORIZED MULTIPLEX SHELF (MS19/WC7, MS19/WC5, MS19/C7 AND MS19/C6), 628003, 628008, 628013 AND 628018 (FOR 19-INCH BAYS)

GENERAL DESCRIPTION

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1. GENERAL

1.01 This Section provides a general description of the ITT D448 multiplex shelf, 628003, 628008, 628013, and 628018 (Fig. 1).

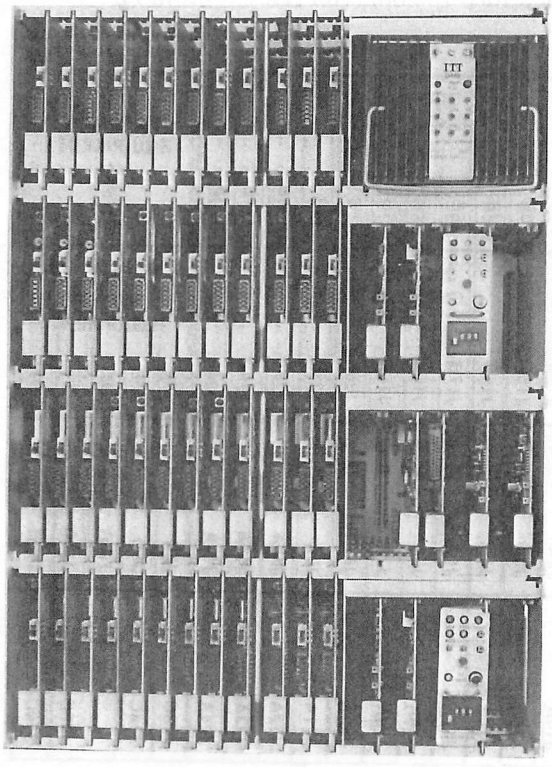

Fig. 1—Connectorized Multiplex Shelf

1.02 This Section has been reissued to add information on the Group -003 multiplex shelf which is used with dataport channel units.

1.03 The shelf provides the mounting for up to 48 D448 channel units (2 digroups) and the D448 common equipment.

2. DESCRIPTION

2.01 The shelf mounts on a 19-inch relay rack and occupies 14 mounting positions. Installation and test procedures for the shelf are provided in Section ITTR-18-406. The channel units and common equipment units which may be mounted in the shelf are listed in Section ITTR-18-001.

2.02 The shelf is available in four versions. The differences in the versions are described in Table A. Each shelf is available in Groups -001 and -003. The shelves are identical, except the Group -003 shelf is dataport compatible and has minor wiring changes to the shelf backplane timing options. The channel unit leads which are available at each VF drop connector are listed in Table B.

TABLE A

PART NO.	MNEMONIC	CONNECTORIZATION FORMAT
628003	MS19/WC7	Universal interface; WE compatible; 7 connectors per digroup
628008	MS19/WC5	Standard interface; WE compatible; 5 connectors per digroup
628013	MS19/C7	Universal interface; 7 connectors per digroup
628018	MS19/C6	Standard interface; 6 connectors per digroup

TABLE B

VF DROP CONNECTOR DESIGNATION	CONNECTORIZED LEADS ON							
	628003		628008		628013		628018	
	TIP	RING	TIP	RING	TIP	RING	TIP	RING
P25A/P25B	E	EB	E	EB	E	M/ATB	E	M/ATB
P26A/P26B	1/S	2	1/S	2	1/S	2	1/S	2
P27A/P27B	T	R	T	R	T	R	T	R
P28A/P28B	M/ATB	MB	M/ATB	MB	EB	MB	EB	MB
P29A/P29B	T1/A	R1/B	T1/A	R1/B	T1/A	R1/B	T1/A	R1/B
P30A/P30B	NT	NR	Not Supplied		NT	NR	Not Supplied	
P31A/P31B	PC/OS	HS	Not Supplied		PC/OS	HS	PC/OS	HS

2.03 The shelf is constructed of die-cast aluminum with sheet-metal sideplates. The backplane consists of an epoxy-glass printed circuit board (PCB) and connectors of various types. The backplane is shown in Fig. 2. An identification label, located on the inside of the left sideplate in the top mounting section of the shelf, indicates the shelf mnemonic (MS19/WC7, MS19/WC5, MS19/C7, or MS19/C6), part number, and issue letter.

2.04 The shelf is functionally divided into three mounting sections. The bottom and top sections (two rows each) provide mounting for Digroup A and Digroup B channel units, respectively. The right section provides mounting for common equipment units. Marking strips are located below each mounting section which identify each mounting position. Mounting positions, identified by connector, are shown in Fig. 3.

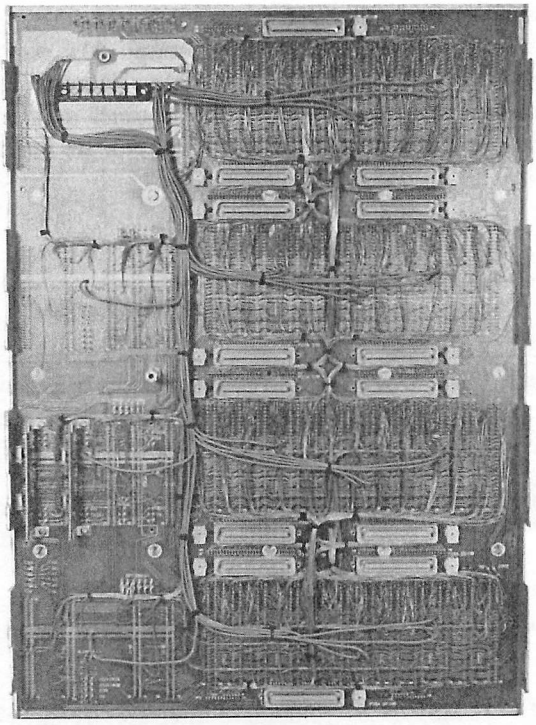

Fig. 2—Connectorized Multiplex Shelf (Backplane)

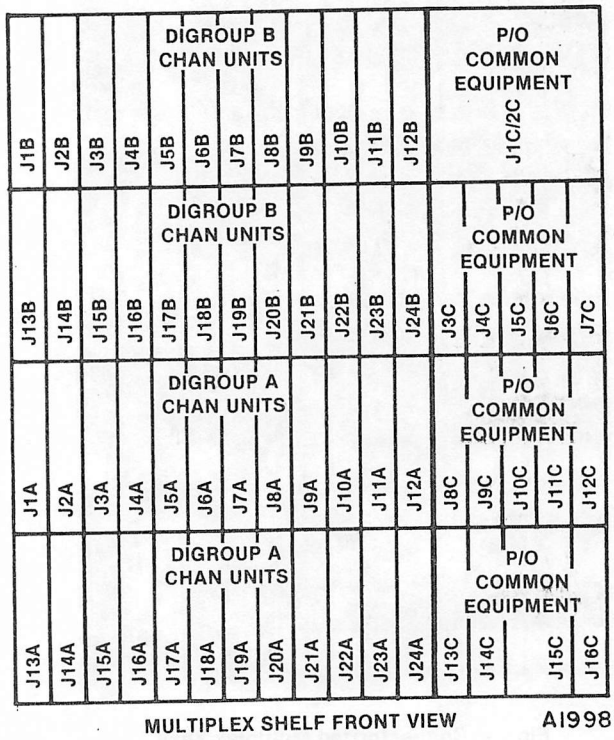
DIGROUP B CHAN UNITS
P/O COMMON EQUIPMENT
J1C/2C
J1B J2B J3B J4B J5B J6B J7B J8B J9B J10B J11B J12B
DIGROUP B CHAN UNITS
P/O COMMON EQUIPMENT
J13B J14B J15B J16B J17B J18B J19B J20B J21B J22B J23B J24B J3C J4C J5C J6C J7C
DIGROUP A CHAN UNITS
P/O COMMON EQUIPMENT
J1A J2A J3A J4A J5A J6A J7A J8A J9A J10A J11A J12A J8C J9C J10C J11C J12C
DIGROUP A CHAN UNITS
P/O COMMON EQUIPMENT
J13A J14A J15A J16A J17A J18A J19A J20A J21A J22A J23A J24A J13C J14C J15C J16C
MULTIPLEX SHELF FRONT VIEW
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Fig. 3—Mounting Positions

2.05 Each common equipment connector is equipped with a polarizing key. This key prevents a unit from being mounted in the wrong shelf position.

2.06 Two central office equalizers (EQLs), 628045-0, are mounted on the DS1 outputs of the shelf (connectors J11C and J12C) for Mode 3 operation. These equalizers may be replaced with equalizers for other cable lengths. An equalizer is mounted on connector J9C when the shelf is used in Mode 1, 2, or 4 operation.

2.07 Factory wiring on the shelf consists of both printed circuit wiring and point-to-point wiring via wire-wrap pins. Customer connections to the shelf are made via 25-pair VF drop connectors (for VF and signaling connections), wire-wrap pins (for alarm, high frequency, and some signaling connections), and a terminal block (for power connections).

3. OPTIONS

3.01 The shelf is equipped with strapping option posts on the backplane for setting alarm timing, ACO operation, channel sequence, CCIS compatibility, and terminal synchronization options. Detailed information on option selection is provided in Section ITTR-18-406.

4. SPECIAL FEATURES

4.01 A circuit assignment card supplied with the shelf not only provides room for listing circuit assignments, but also for listing the mode of operation and the above option settings. This card may be mounted either in the shelf or on the rack flange. Installation information is provided in Section ITTR-18-406.

4.02 Labels on the inside of the backplane cover show the locations of the backplane strapping option posts and give instructions on setting these options.

5. TECHNICAL SUMMARY

5.01 A summary of technical specifications for the multiplex shelf is given in Table C.

TABLE C

ITEM	SPECIFICATION
DIMENSIONS	
Height Width Depth	24.5 in. (61.3 cm) 19.0 in. (47.5 cm) 12.0 in. (30.5 cm)
WEIGHT (Empty)	22.0 lb (10.0 kg)

6. REPLACEABLE PARTS

6.01 The multiplex shelf is equipped with two field-replaceable C.O. equalizers, 628045-0. Refer to Section ITTR-81-045 for detailed information on C.O. equalizers.

7. REFERENCES

7.01 The engineering drawings listed in Table D were used in the preparation of this Section.

TABLE D

TYPE	DRAWING NO.
Assembly	628003-000
Assembly	628008-000
Assembly	628013-000
Assembly	628018-000

7.02 Related information on the multiplex shelf may be found in the ITTR Sections referenced in Table E.

TABLE E

TITLE	SECTION NO.
D448 PCM Trunk Carrier System — General Description	ITTR-18-001
Central Office Equalizer (EQL), 628045 — General Description	ITTR-18-045
Connectorized Multiplex Shelf (MS19/WC7, MS19/WC5, MS19/C7 and MS19/C6), 628003, 628008, 628013 and 628018 (For 19-Inch Bays) — Option Selection, Installation and Pretest	ITTR-18-406